

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) An arrangement, comprising:
an integrated circuit[[,]] having a housing with a plurality of connector pins;
a printed circuit board having conducting paths, ~~to which the integrated circuit is electrically~~
and mechanically contacted by the connector pins; and
at least one blocking capacitor, ~~which is switched into a power supply path for the integrated~~
circuit,
wherein the blocking capacitor, ~~of which there is at least one,~~ is spatially arranged between
the connector pins ~~of the housing~~ and is electrically contacted to the connector pins, and the
connector pins are inserted through openings of a carrier that is arranged between the housing and
the printed circuit board, and
wherein the blocking capacitor is positioned on a side of the carrier facing away from the
housing.

2. (Original) The arrangement according to claim 1, wherein the integrated circuit has a
ball grid array housing with ball-shaped connector pins.

3. (Canceled).

4. (Currently Amended) The arrangement according to claim [[3]] 1, wherein the
openings are in the form of bore holes.

5. (Canceled).

6. (Canceled).

7. (Currently Amended) A heat-resistant foil carrier, comprising:

openings[[,]] through which connector pins of a housing surrounding an integrated circuit can be inserted; and

at least one blocking capacitor,~~which is~~ mounted on the carrier between the openings.

8. (Currently Amended) A carrier, comprising:
openings[[,]] through which connector pins of a housing surrounding an integrated circuit can be inserted; and

at least one blocking capacitor,~~which is~~ inserted into the carrier between the openings, such that the blocking capacitor is centrally positioned compared to the carrier sides.

9. (Original) The carrier according to claim 7, wherein the openings are in the form of bore holes.

10. (Original) The carrier according to claim 7, wherein the blocking capacitors are bonded onto the carrier.

11. (Canceled).

12. (Currently Amended) The arrangement according to claim 1, wherein ~~a carrier comprising openings, through which connector pins of a housing surrounding an integrated circuit can be inserted, and at least one~~ the blocking capacitor, ~~which~~ is mounted on the carrier between the openings[[,]] and is introduced between the housing and printed circuit board.

13. (Currently Amended) A method for assembling at least one blocking capacitor, comprising:

arranging the ~~at least one~~ blocking capacitor in a current supply path for an integrated circuit[[,]] having a housing with a plurality of connector pins, ~~which~~ that are electrically and mechanically contacted to conducting paths of a printed circuit board arranged in proximity to the housing; and

spatially arranging the ~~at least one~~ blocking capacitor between the printed circuit board and the integrated circuit ~~and is contacted to~~ so that the blocking capacitor contacts the connector pins ~~of the housing of the integrated circuit,~~

wherein the connector pins are inserted through openings of a carrier that is arranged between the housing and the printed circuit board, and the blocking capacitor is positioned on a side of the carrier facing away from the housing.

14. (Original) The method according to claim 13, wherein the blocking capacitor, of which there is at least one, is assembled before assembly onto or into a carrier between openings thereof.

15. (Original) The method according to claim 14, wherein the connector pins are inserted through the openings of the carrier positioned between the housing and the printed circuit board.

16. (Original) The carrier according to claim 8, wherein the openings are in the form of bore holes.

17. (Original) The carrier according to claim 8, wherein the blocking capacitors are bonded onto the carrier.

18. (Original) The carrier according to claim 8, wherein the carrier is made out of a heat-resistant foil.

19. (Currently Amended) ~~The arrangement according to claim 1,~~ An arrangement, comprising:

an integrated circuit having a housing with a plurality of connector pins;
a printed circuit board having conducting paths electrically and mechanically contacted by the connector pins;

a carrier having openings through which the connector pins can be inserted; and
at least one blocking capacitor switched into a power supply path for the integrated circuit,

wherein the blocking capacitor is spatially arranged between the connector pins and is electrically contacted to the connector pins, and

~~wherein a carrier comprising openings, through which connector pins of a housing surrounding an integrated circuit can be inserted, and at least one blocking capacitor, which~~ the blocking capacitor ~~is inserted into the carrier between the openings, such that the blocking capacitor is centrally positioned compared to the carrier sides[[,]]~~ and ~~is introduced between the housing and printed circuit board.~~